



# OpenSPARC Workshop Brussels, Belgium

• April 23 & 24 2009



# Agenda

<b>Day 1</b>	<b>Thursday April 23</b>	
<b>10:00</b>	Registration & Coffee	
<b>10:15</b>	Opening remarks & introductions	Shrenik Mehta, Sun Microsystems
<b>10:30</b>	Welcome	John McLean, PhD. Europractice Software Service
<b>10:50</b>	Chip Multi-Threading and OpenSPARC Architecture Overview	Rick Hetherington, Sun Microsystems
<b>12:30</b>	Lunch	All
<b>13:30</b>	T1 and T2 Overview	Rick Hetherington, Sun
<b>14:30</b>	OpenSPARC Program	Shrenik Mehta, Sun
<b>15:00</b>	Break	All
<b>15:15</b>	Simulators	Shrenik Mehta, Sun
<b>16:15</b>	Synthesizing OpenSPARC design in 90nm technology library	Vazgen Melikyan, Synopsys

# Agenda 2

<b>Day 2</b>	<b>Friday April 24</b>	
<b>9:00</b>	Hypervisor and Virtualisation	Darryl Gove, Sun
<b>10:00</b>	Community Participation	Shrenik Mehta, Sun
<b>10:30</b>	Break	
<b>10:45</b>	Compiler and Open Solaris	Darryl Gove, Sun
<b>12:15</b>	Lunch	All
<b>13:15</b>	OpenSPARC FPGA Overview	Didier Heck, Sun
<b>14:15</b>	Virtex-6 FPGA Overview	Marc Defossez, Xilinx
<b>15:15</b>	<b>End</b>	<b>All</b>

Download at: <http://www.opensparc.net/whats-new/2009-q1/free-introductory-workshop.html>